



PATENT

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hung Cai Ngo

Serial No.: 10/733,933

Date Filed: December 10, 2003

Title: Methods and Arrangements  
for a Low Power Phase-  
Locked Loop

§ Group Art Unit: 2816

§ Examiner: Luu, An T

§ Confirmation No.: 8706

§ IBM Docket No.: AUS920030779US1

§ Atty Docket No.: IBM.4019.PAT

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## COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Applicant appreciates the Examiner's reasons for allowance. Applicant respectfully submits that the claims are allowable because the art of record fails to teach, alone or in combination, the invention as described in each of the independent claims.

Respectfully submitted,

June 3, 2005

Date

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